



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
 United States Patent and Trademark Office
 Address: COMMISSIONER FOR PATENTS
 P.O. Box 1450
 Alexandria, Virginia 22313-1450
 www.uspto.gov



Bib Data Sheet

CONFIRMATION NO. 5389

SERIAL NUMBER 10/725,320	FILING DATE 12/02/2003 RULE	CLASS 438	GROUP ART UNIT 2891	ATTORNEY DOCKET NO. P69335US0
-----------------------------	---------------------------------------	--------------	------------------------	-------------------------------------

APPLICANTS

Kwan-Yong Lim, Ichon-shi, KOREA, REPUBLIC OF;

Heung-Jae Cho, Ichon-shi, KOREA, REPUBLIC OF;

Jung-Ho Lee, Ichon-shi, KOREA, REPUBLIC OF; Se-Aug Jang, Ichon-shi, KOREA, REPUBLIC OF;

Yong-Soo Kim, Ichon-shi, KOREA, REPUBLIC OF;

Byung-Seop Hong, Ichon-shi, KOREA, REPUBLIC OF;

Jae-Geun Ho, Ichon-shi, KOREA, REPUBLIC OF;

Hong-Seon Yang, Ichon-shi, KOREA, REPUBLIC OF;

Hyun-Chul Sohn, Ichon-shi, KOREA, REPUBLIC OF;

IA
 ** CONTINUING DATA *****

IA
 ** FOREIGN APPLICATIONS *****

REPUBLIC OF KOREA 2003-43087 06/30/2003

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 02/27/2004

Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Examiner's Signature <i>[Signature]</i> Initials <i>IA</i>	STATE OR COUNTRY KOREA, REPUBLIC OF	SHEETS DRAWING 8	TOTAL CLAIMS 15	INDEPENDENT CLAIMS 2
--	--	---	----------------------------	---------------------------	--------------------------------

ADDRESS

43569

MAYER, BROWN, ROWE & MAW LLP

1909 K STREET, N.W.

WASHINGTON, DC

20006

TITLE

Method for fabricating gate-electrode of semiconductor device with use of hard mask

☐ All Fees

☐ 1.16 Fees (Filing)